



# Embedded Microcontroller Interfacing for M-CORE Systems (Engineering)

*G. Jack Lipovski*

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The "M-CORE" family of microprocessors is the latest 32-bit integrated circuit from Motorola designed to be a multi-purpose "micro-controller." The processor architecture has been designed for high performance and cost-sensitive embedded control applications with particular emphasis on reduced power consumption. This is the first book on the programming of the new language instruction set using the M-CORE chip.

**Embedded Microcontroller Interfacing for M-CORE Systems** is the third of a trio of books by G. Jack Lipovski from the University of Texas. The first two books are on assembly language programming for the new Motorola 6812 16-bit microcontroller, and were written to be textbooks and professional references. This book was written at the request of the Motorola design team for the professional users of its new and very successful M-CORE chip microcontrollers. Written with the complete cooperation and input of the M-CORE design engineers at their headquarters in Austin, Texas, this book covers all aspects of the programming software and hardware of the M-CORE chip.

- \* First introductory level book on the Motorola MoCORE
- \* Teaches engineers how a computer executes instructions
- \* Shows how a high-level programming language converts to assembler language
- \* Teaches the reader how a microcontroller is interfaced to the outside world
- \* Hundreds of examples are used throughout the text
- \* Over 200 homework problems give the reader in-depth practice
- \* A CD-ROM with HIWARE's C++ compiler is included with the book
- \* A complete summary chapter on other available microcontrollers

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